

# Timing performance of large area SiPMs coupled to LYSO using noise compensation methods

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<http://srs.fbk.eu>

**Goal:** To optimize Time-of-Flight capability of FBK SiPMs coupled to “slow” scintillators (e.g. LYSO)

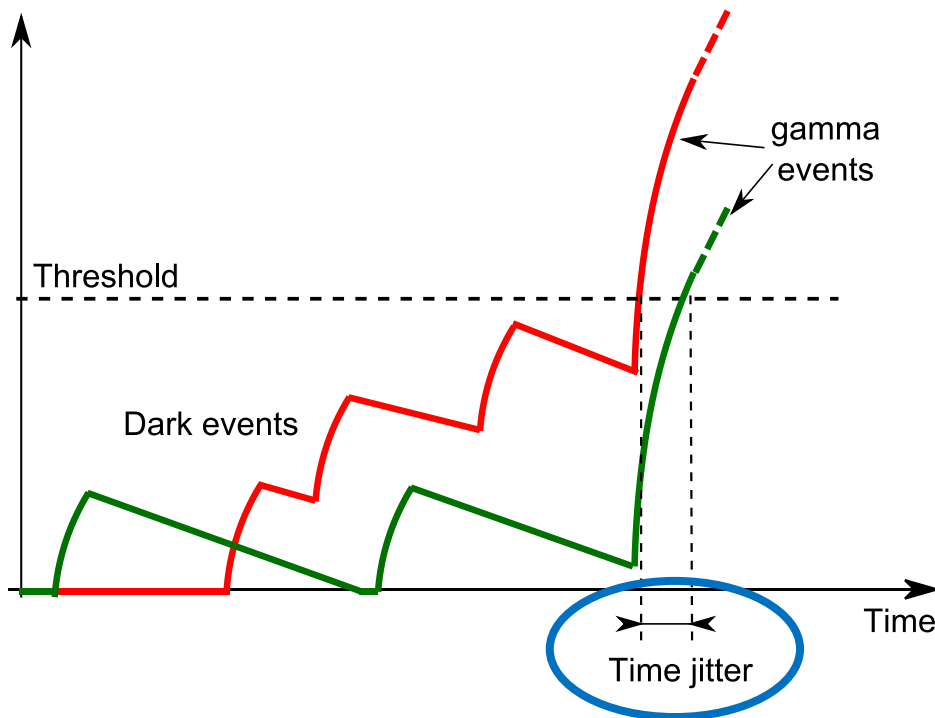
Main concerns for large area SiPMs were/are:  
the **high dark noise**,  
the **signal shape**,  
the **capacitance**...

In the past year we have worked on **a method, implementable in an ASIC, which minimizes the effect of dark noise** on the timing performance.

In this contribution we will show some (quite good) results obtained with FBK large area SiPMs.

# Effect of dark noise in LED

Leading Edge Discriminator (LED) is commonly used for time pick-off with PMTs.



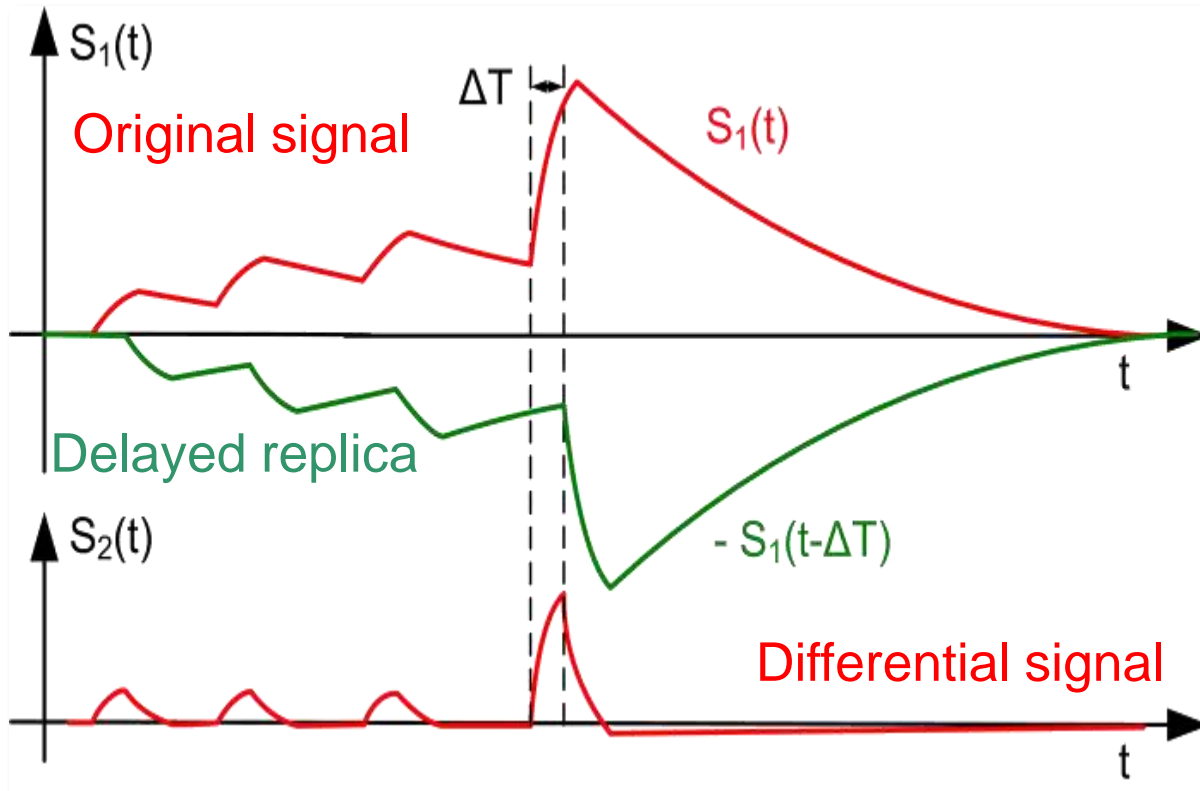
long tail of the dark events

high dark rate

**baseline fluctuation**  
→ **time jitter**

**high LED threshold**  
→ **worse photon stat.**

In large area SiPMs the dark rate can be quite high and consequently also the effect described above.



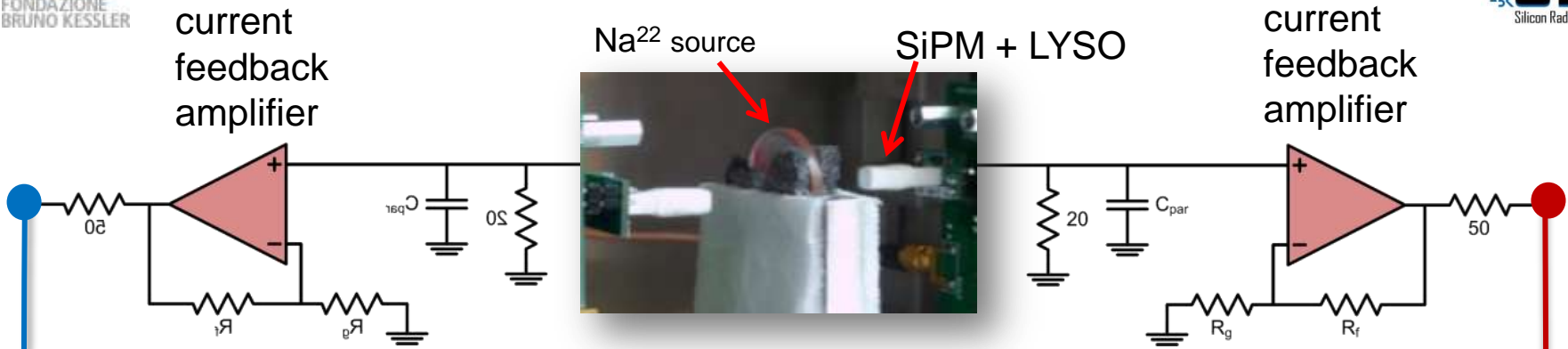
We exploit the difference between rise time and decay time to obtain a signal:

- “free” from baseline fluctuations
- identical initial part of the gamma signal

Then, we use the LED on the differential signal  $s_2(t)$ .

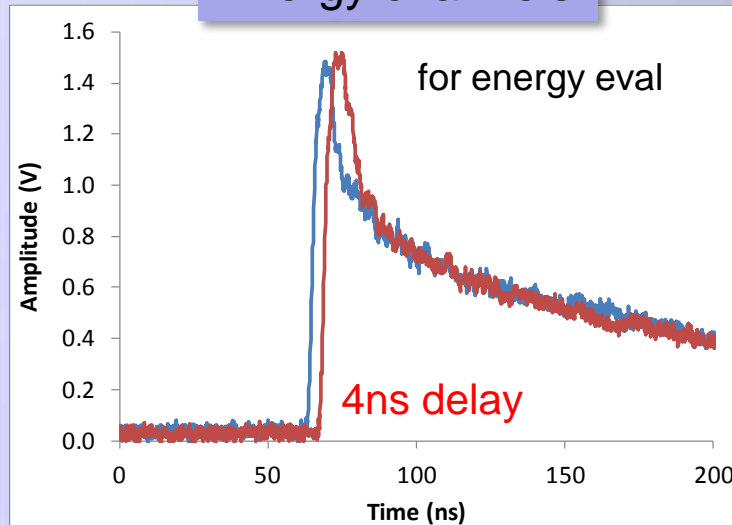
**Important:** electronic noise is  $\sim \sqrt{2}$  higher in differential signal so its effect must be negligible for DLED to be effective

# Experimental set-up

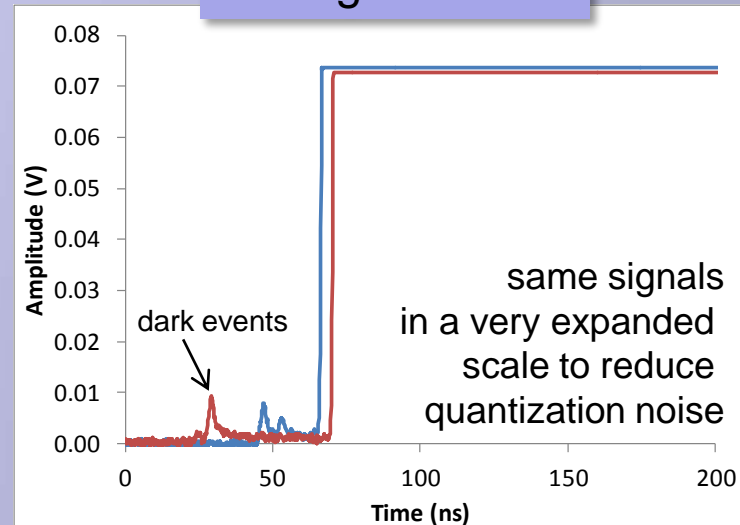


## Digital scope (1GHz, 10GSa/s, 8 bit ADC)

### Energy channels

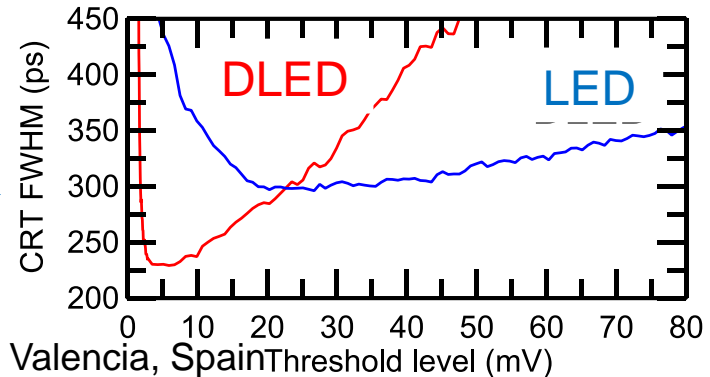
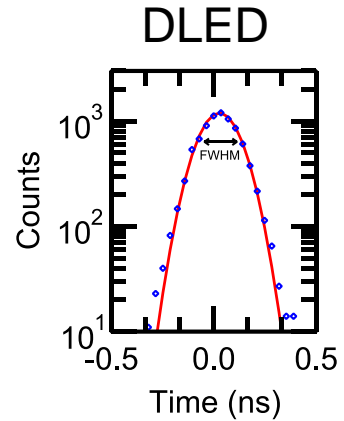
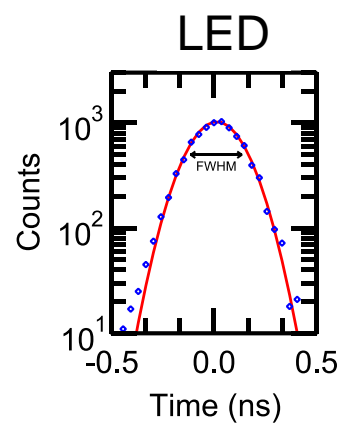
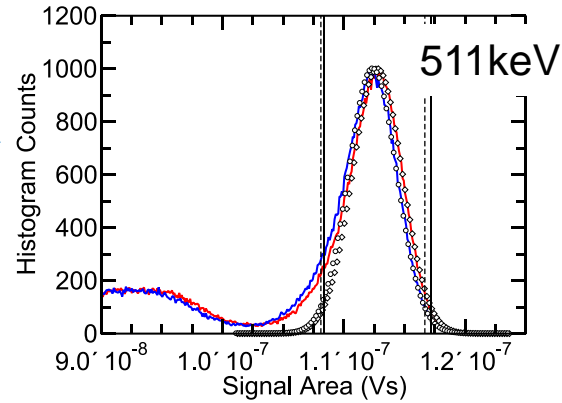
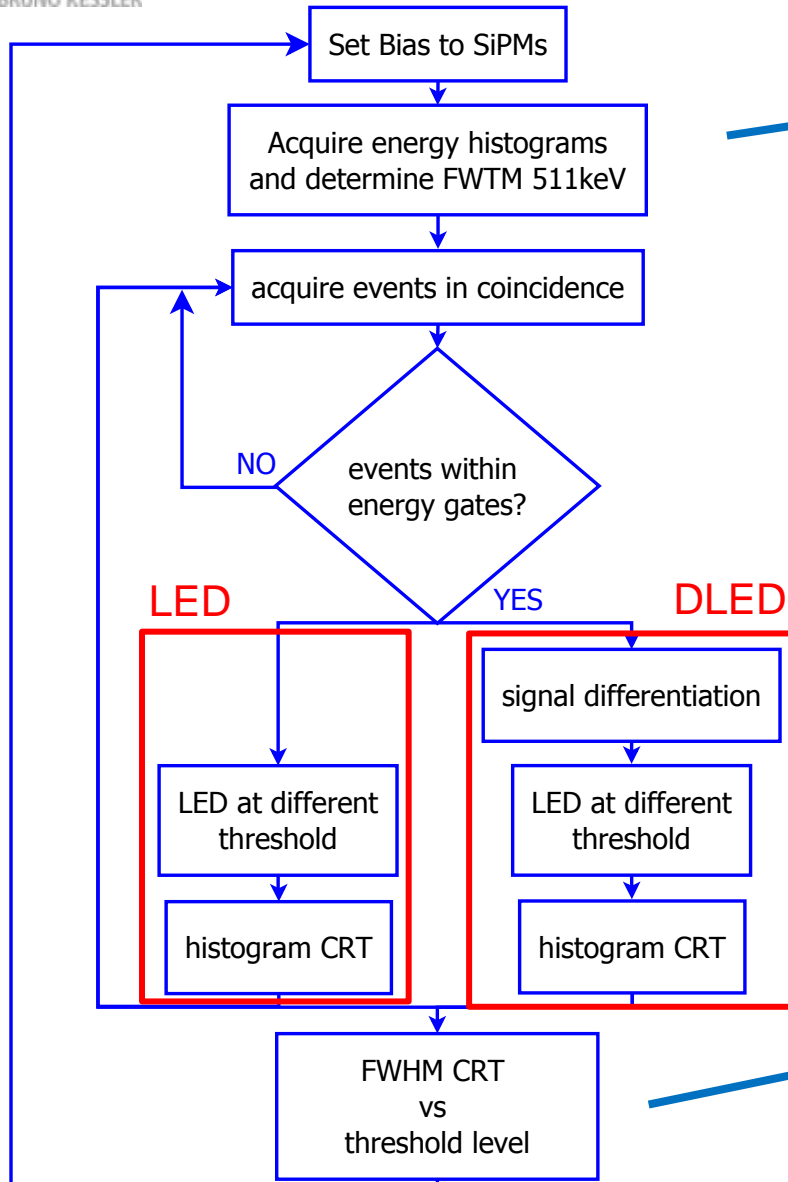


### Timing channels

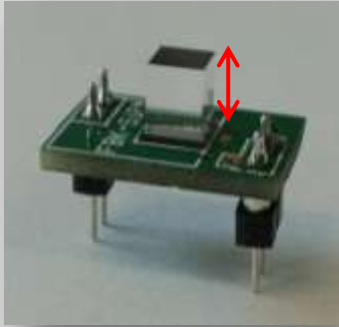


Ch1 1MΩ Ch2 Ch3 50Ω Ch4

# Measurement procedure



## Detector “cube”

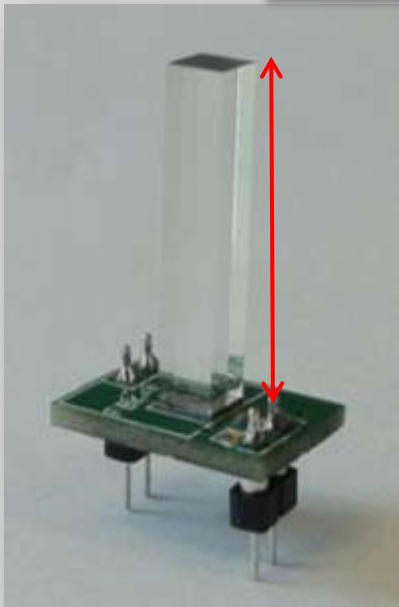


**LYSO crystal  $3 \times 3 \times 5 \text{mm}^3$**

height  $\sim$  side

to test ultimate SiPM performance

## Detector “PET”



**LYSO crystal  $3 \times 3 \times 15 \text{mm}^3$**

height  $\sim$  5 x side

real PET configuration  
(timing affected by light propagation in crystal)

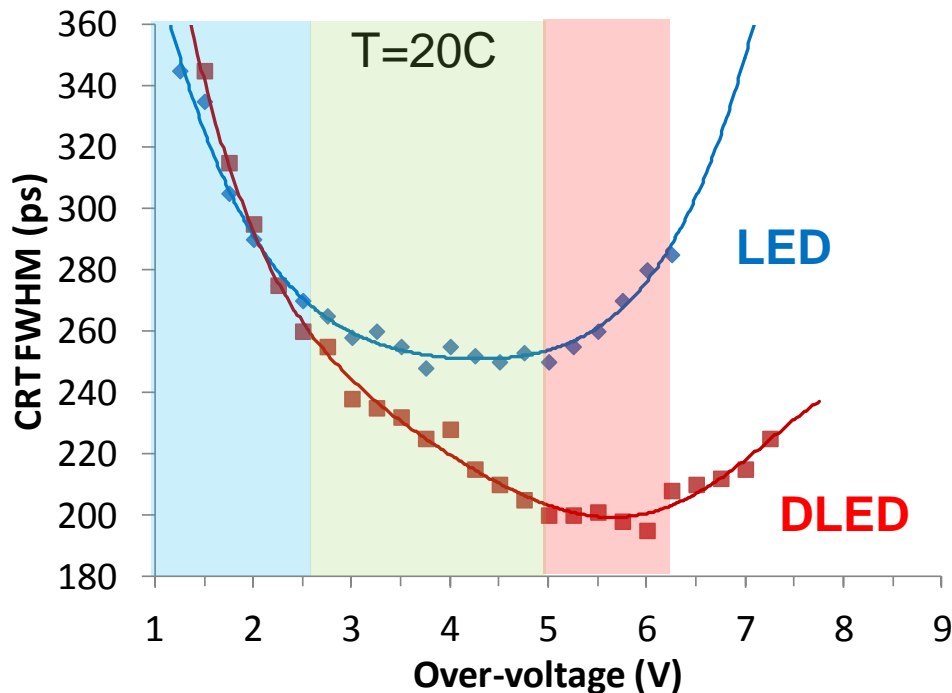
## SiPMs used

- $3 \times 3 \text{mm}^2$
- $4 \times 4 \text{mm}^2$

67um cell-size

produced by FBK, Trento

**Detector cube,  
3x3mm<sup>2</sup> SiPM**



**DLED  $\Delta T=500$ ps  
good up to 1ns**

## Low over-voltage:

low gain, low dark rate

- ➔ electronic noise dominates
- ➔ DLED slightly worse than LED

## Medium over-voltage:

gain increases

dark noise ampl. > elect. noise

- ➔ LED is flat (increase of PDE is compensated by increase of noise)
- ➔ DLED improves following PDE

## High over-voltage:

high dark noise/rate

- ➔ LED starts deteriorating
- ➔ DLED still improves for high PDE and good noise compensation

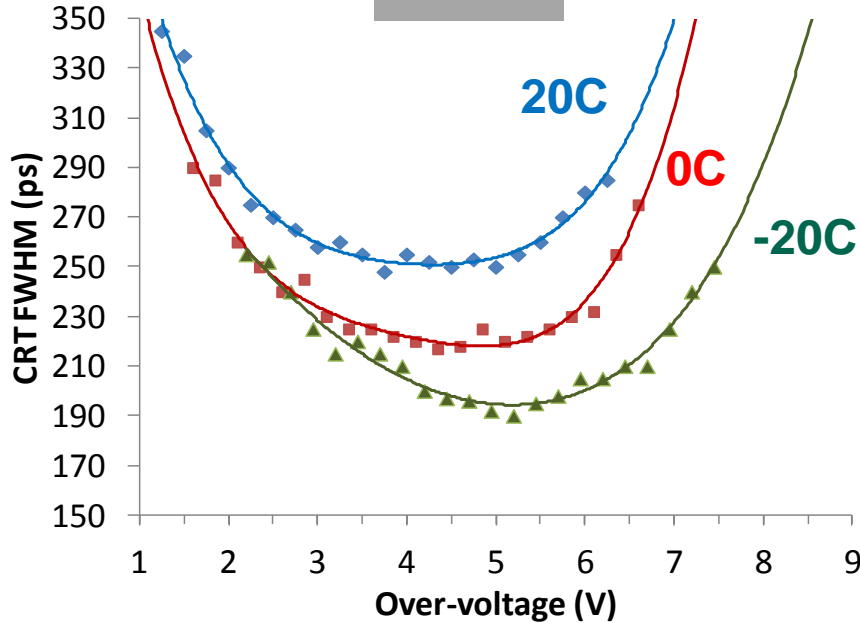


## In the next slides:

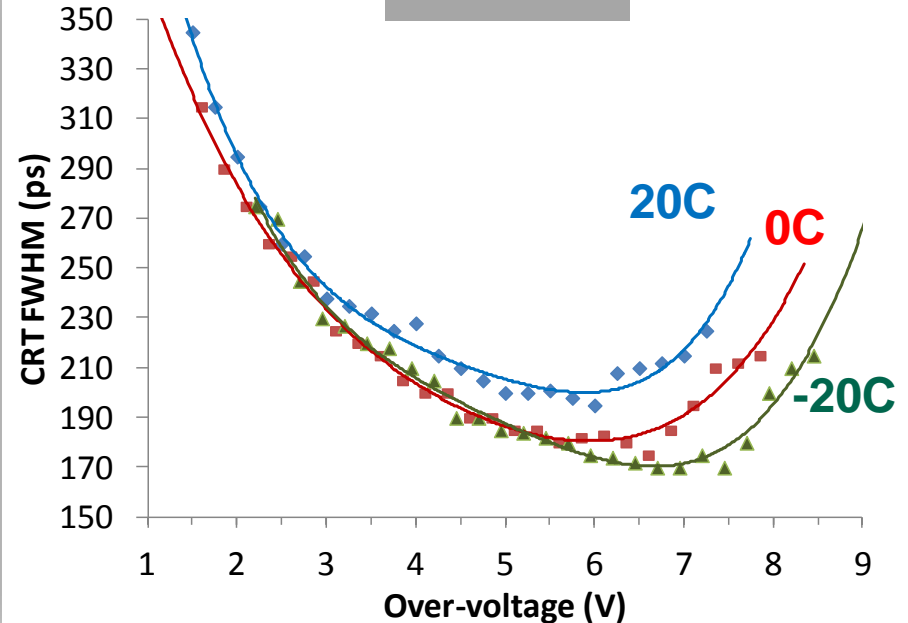
- **CRT vs Temperature**
- **Threshold level**
- **crystal height**
- **SiPM size**

**Detector cube  
3x3mm<sup>2</sup> SiPM**

**LED**



**DLED**



LED strongly improves with temperature because of noise reduction.

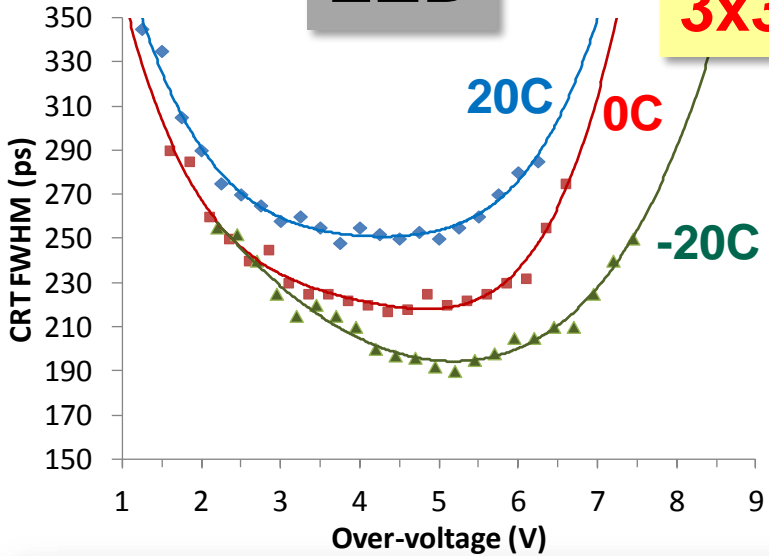
DLED improves less with temperature and only at high over-voltages.

**LED @ -20C is nearly equivalent to DLED @ 20C**

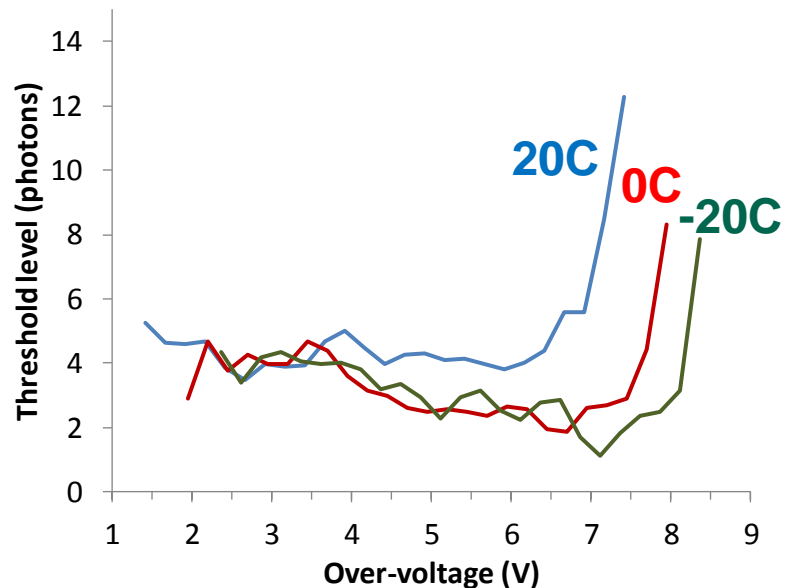
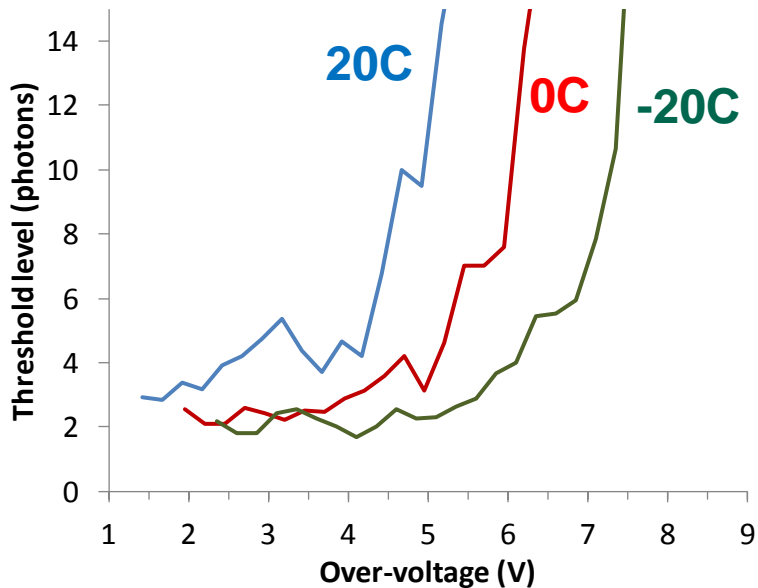
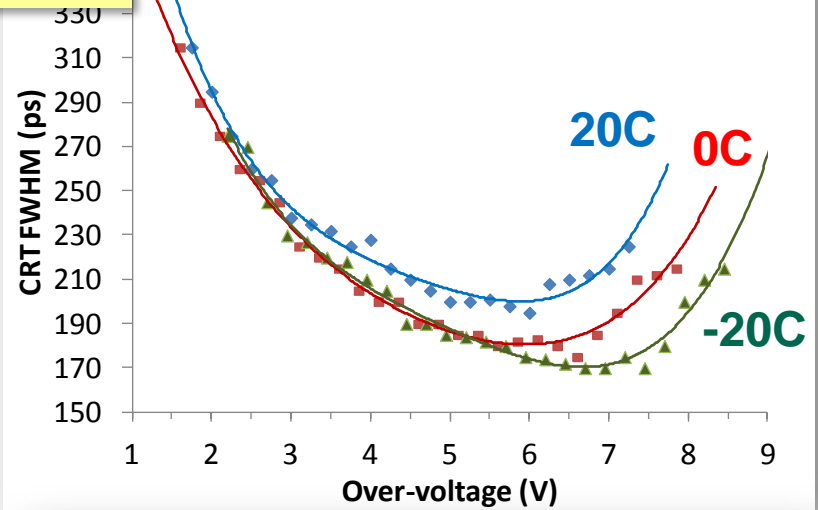
# Optimum Threshold level

**Detector cube  
3x3mm<sup>2</sup> SiPM**

**LED**



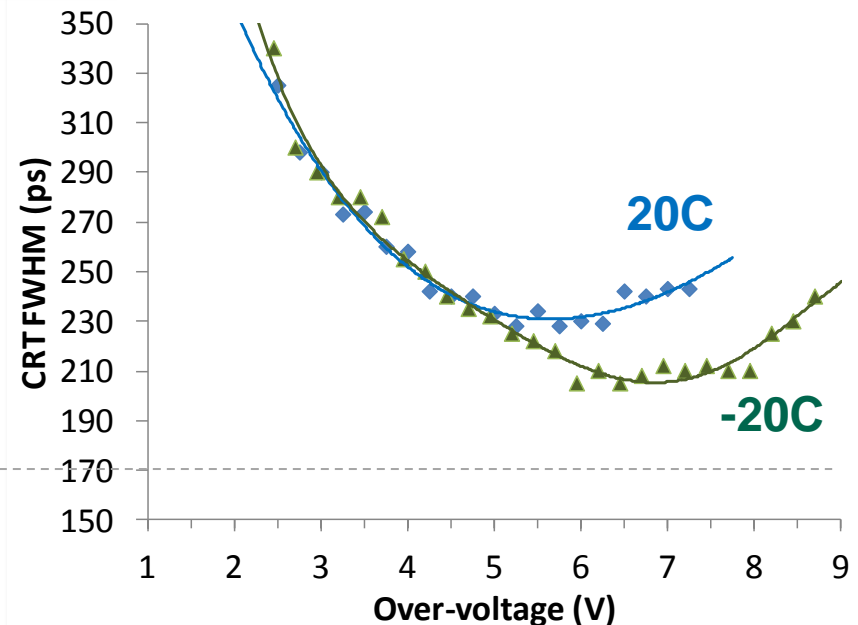
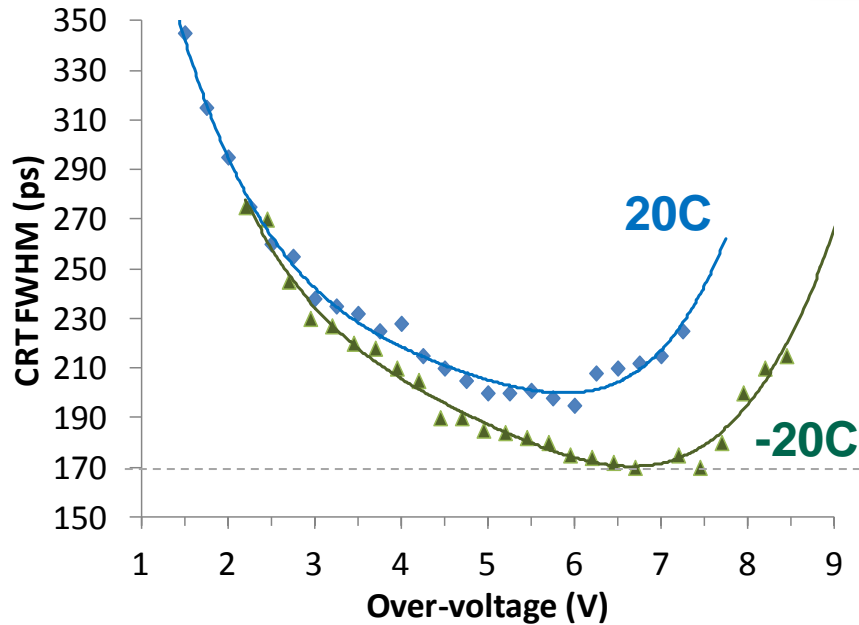
**DLED**



Detector cube

DLED

Detector PET

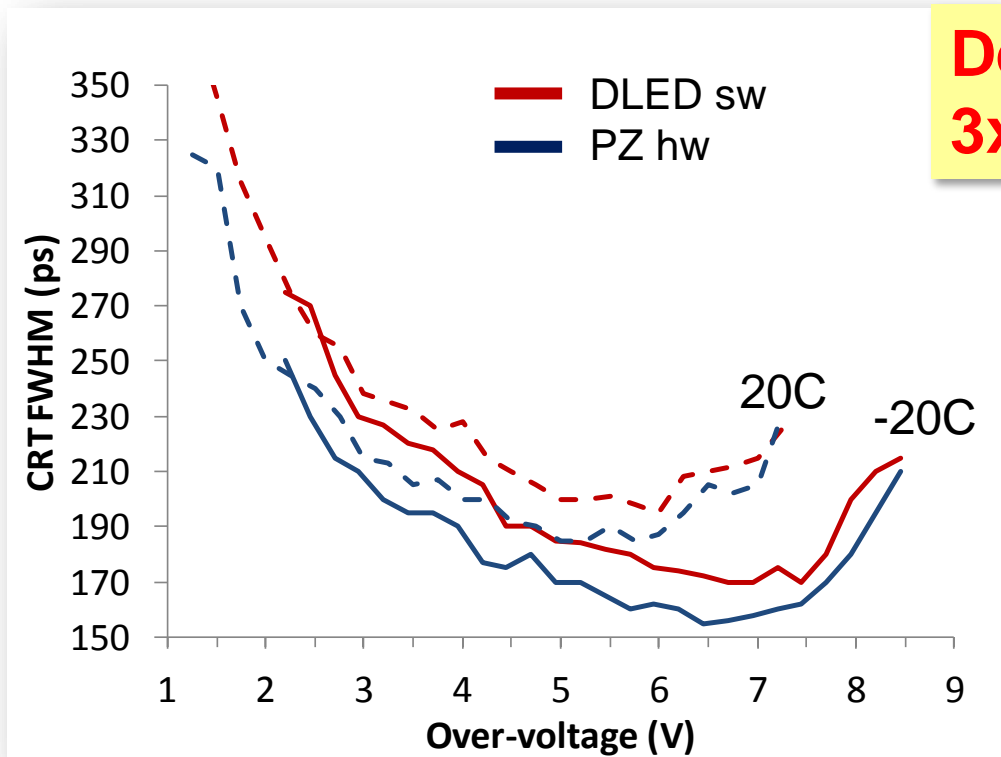


As expected, with thicker crystal the timing performance degrades, still reaching 230ps at 20C.

## CRT FWHM (ps)

	T = 20C		T = -20C	
	3x3mm <sup>2</sup> SiPM	4x4mm <sup>2</sup> SiPM	3x3mm <sup>2</sup> SiPM	4x4mm <sup>2</sup> SiPM
<b>Cube</b> (3x3x5mm <sup>3</sup> LYSO)	200	190	170	165
<b>PET</b> (3x3x15mm <sup>3</sup> LYSO)	230	230	210	210

We implemented a simple hardware baseline compensation to be implemented eventually in an ASIC. Results are very promising, even better than DLED sw.



**Detector cube  
3x3mm<sup>2</sup> SiPM**

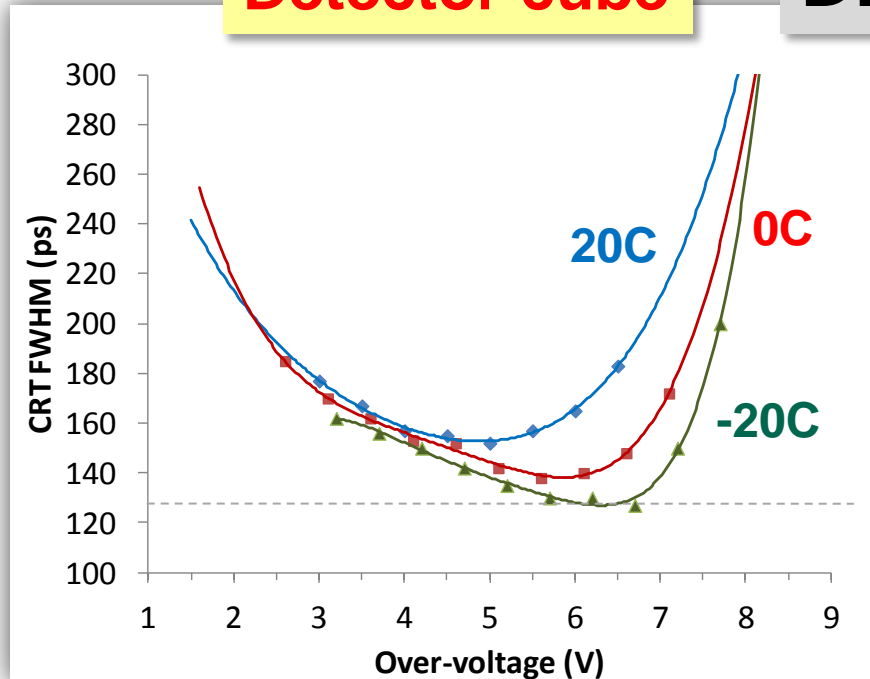
**Details in poster  
n. NP2.S-51**

New 2.2x2.2mm<sup>2</sup> SiPM with enhanced fill factor has been produced by FBK: ~65% on a 50x50um<sup>2</sup> cell.

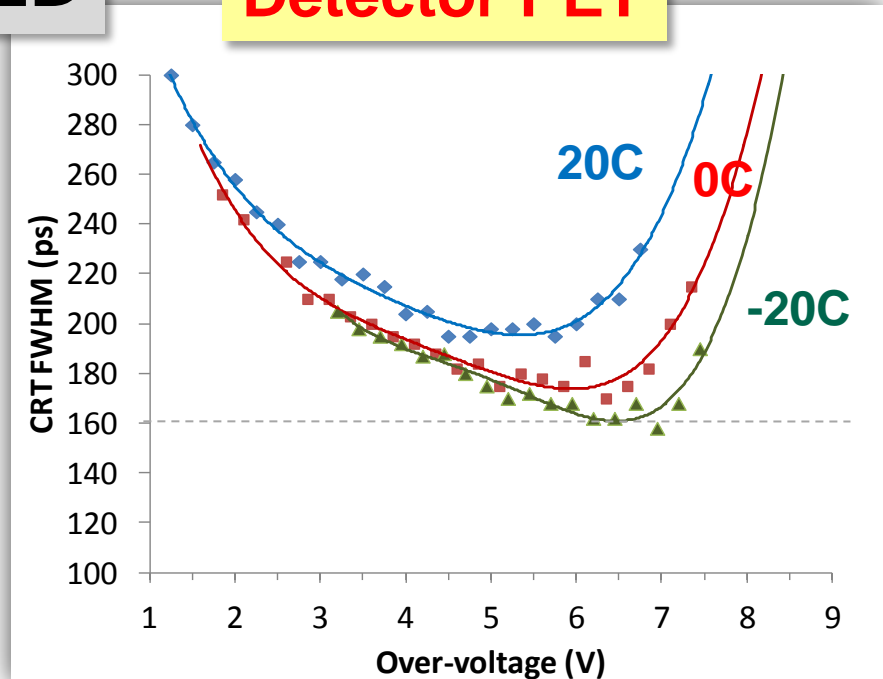
Detector cube

DLED

Detector PET



LYSO: 1.8x1.8x2mm<sup>3</sup>



LYSO: 1.8x1.8x10mm<sup>3</sup>

Quite impressive results!!

We demonstrated that the **effect of dark noise** on timing measurements with SiPM coupled to LYSO crystal **can be largely compensated.**

**FBK technology allows a very competitive TOF performance** that can lead to strong improvements in TOF-PET compared to present systems

In poster **NP2.S-51** we show that a **simple hardware noise compensation can be implemented in an ASIC**

New developments are on-going to reach even better values...



HyperImage project



SUBLIMA project



FBK-INFN MEMS2 agreement